

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of)
YAMAMOTO et al.)
Application Number: To be Assigned)
Filed: Concurrently Herewith)
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE)
AND MANUFACTURING METHOD THEREOF)
ATTORNEY DOCKET NO. HITA.0536)

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Applicant has amended the claims in this application in accordance with standard U.S. practice. Prior to an examination on the merits, please amend the above-identified application as follows: